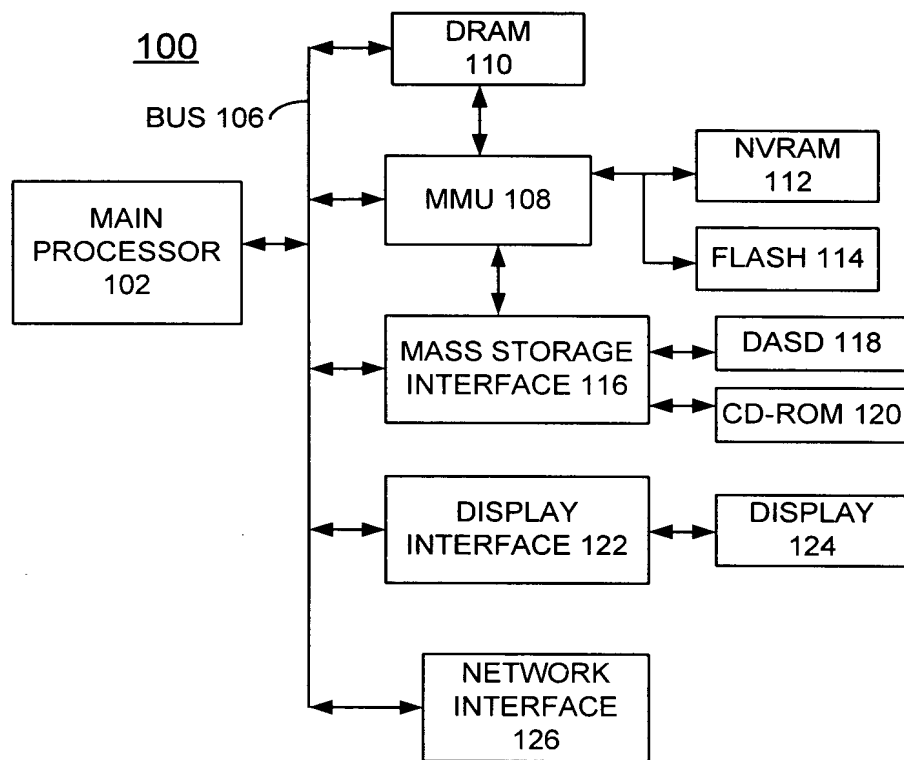


FIG. 1A



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100

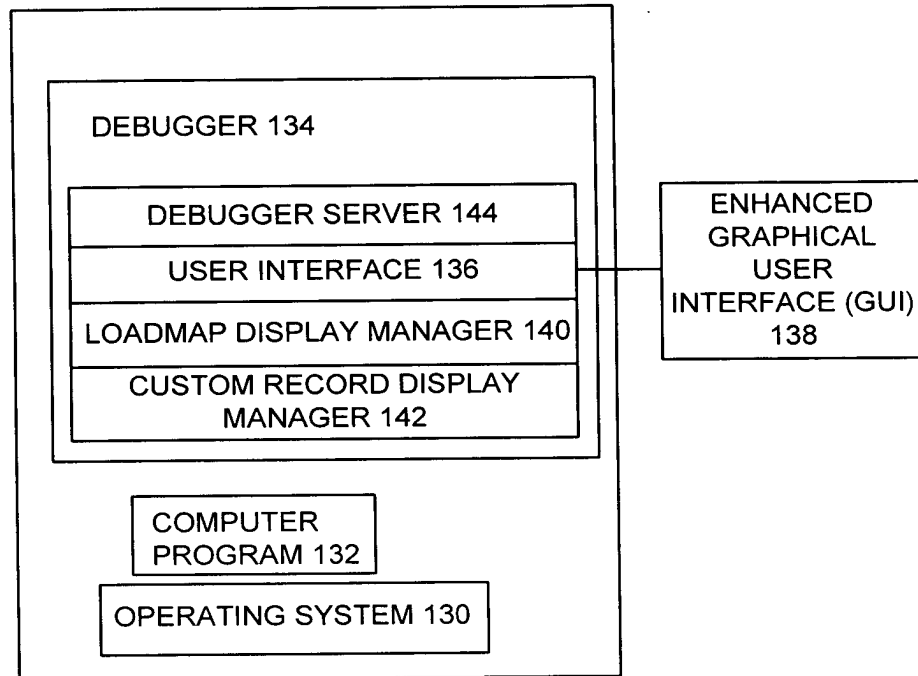
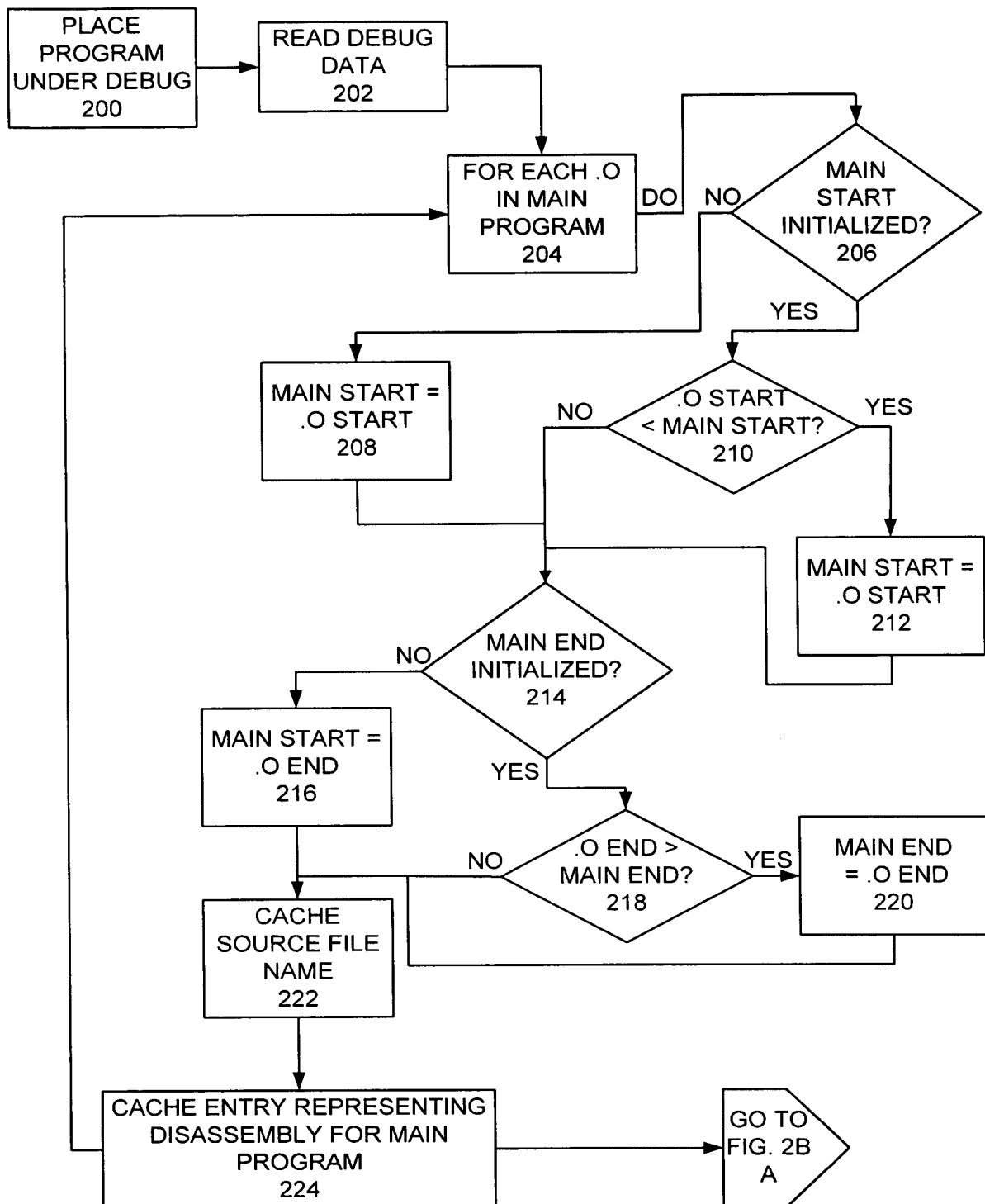
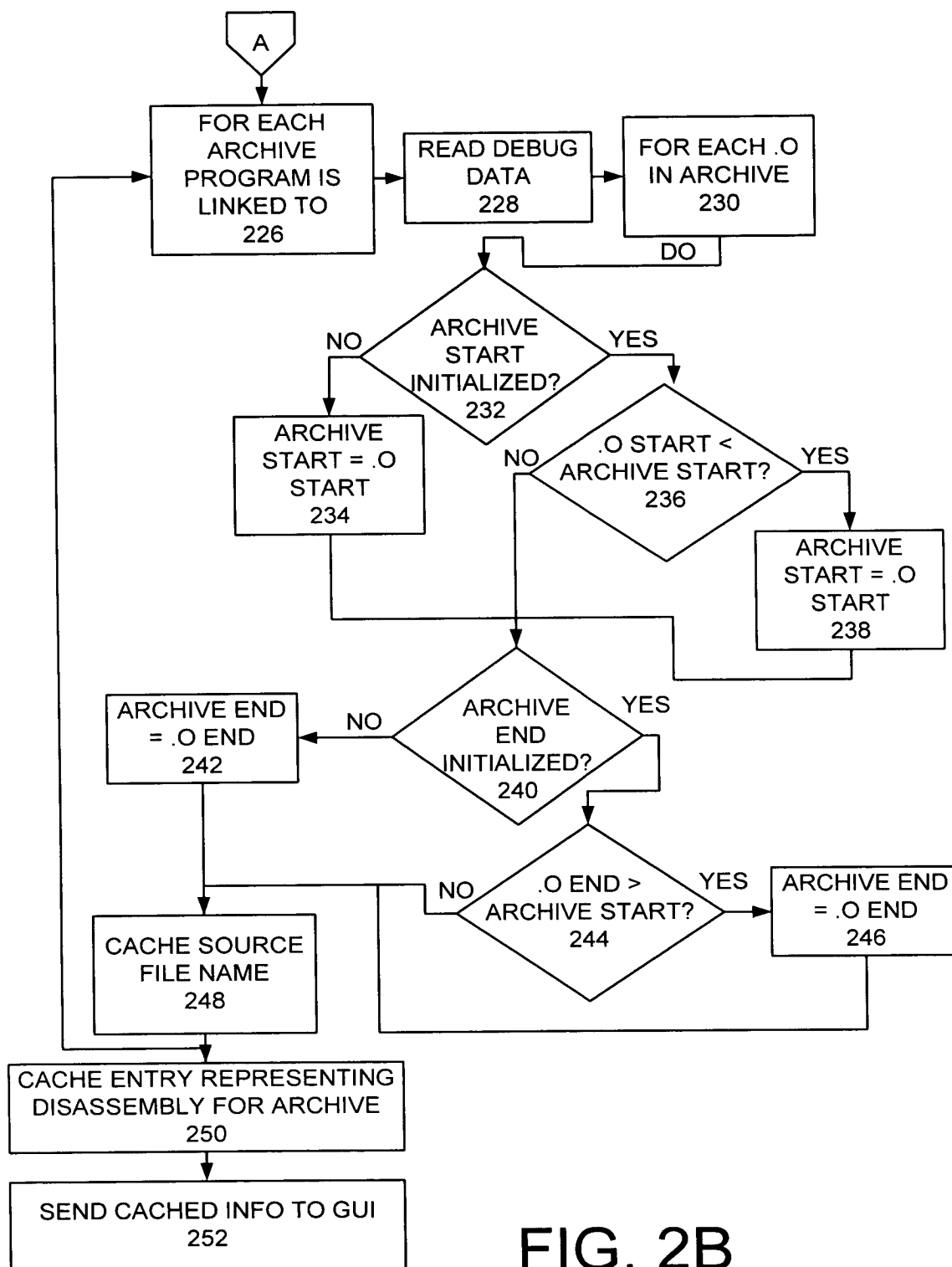


FIG. 1B

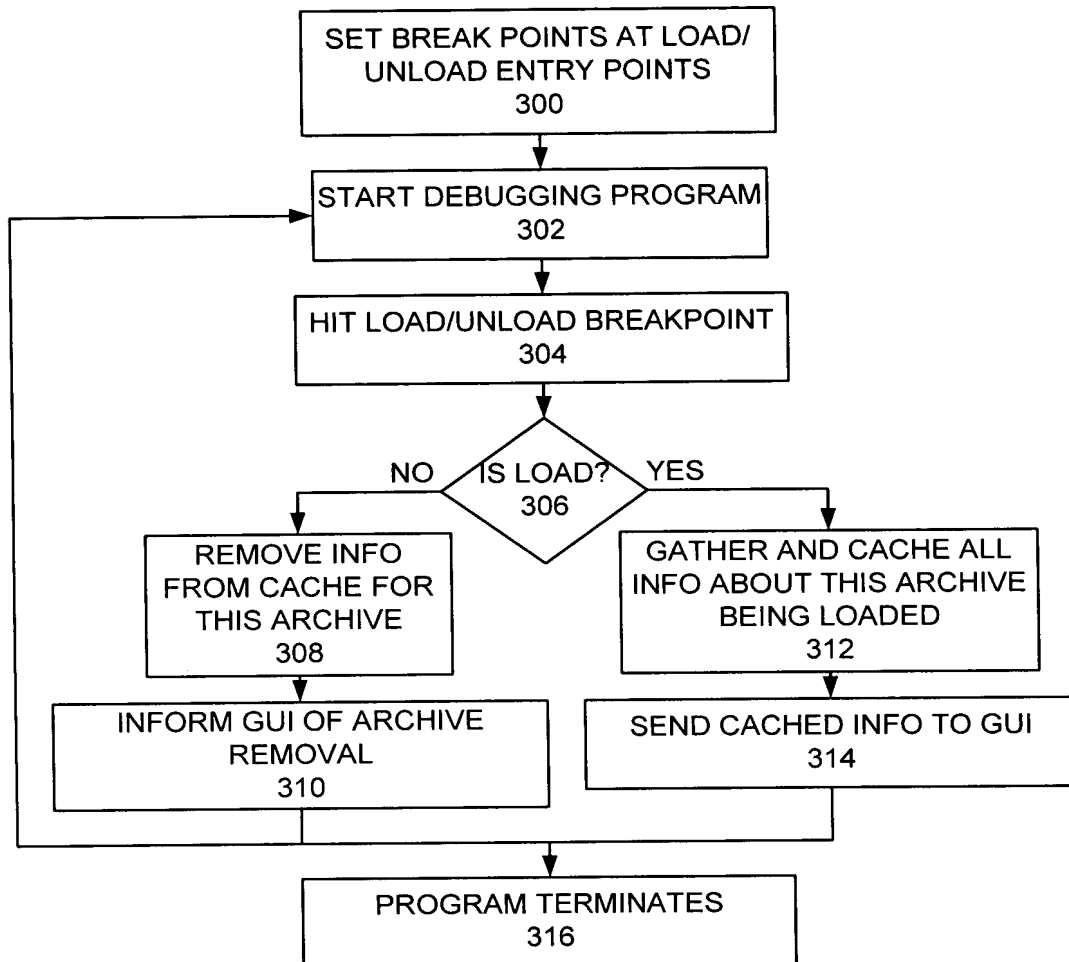
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FIG. 2A

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FIG. 2B

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FIG. 3

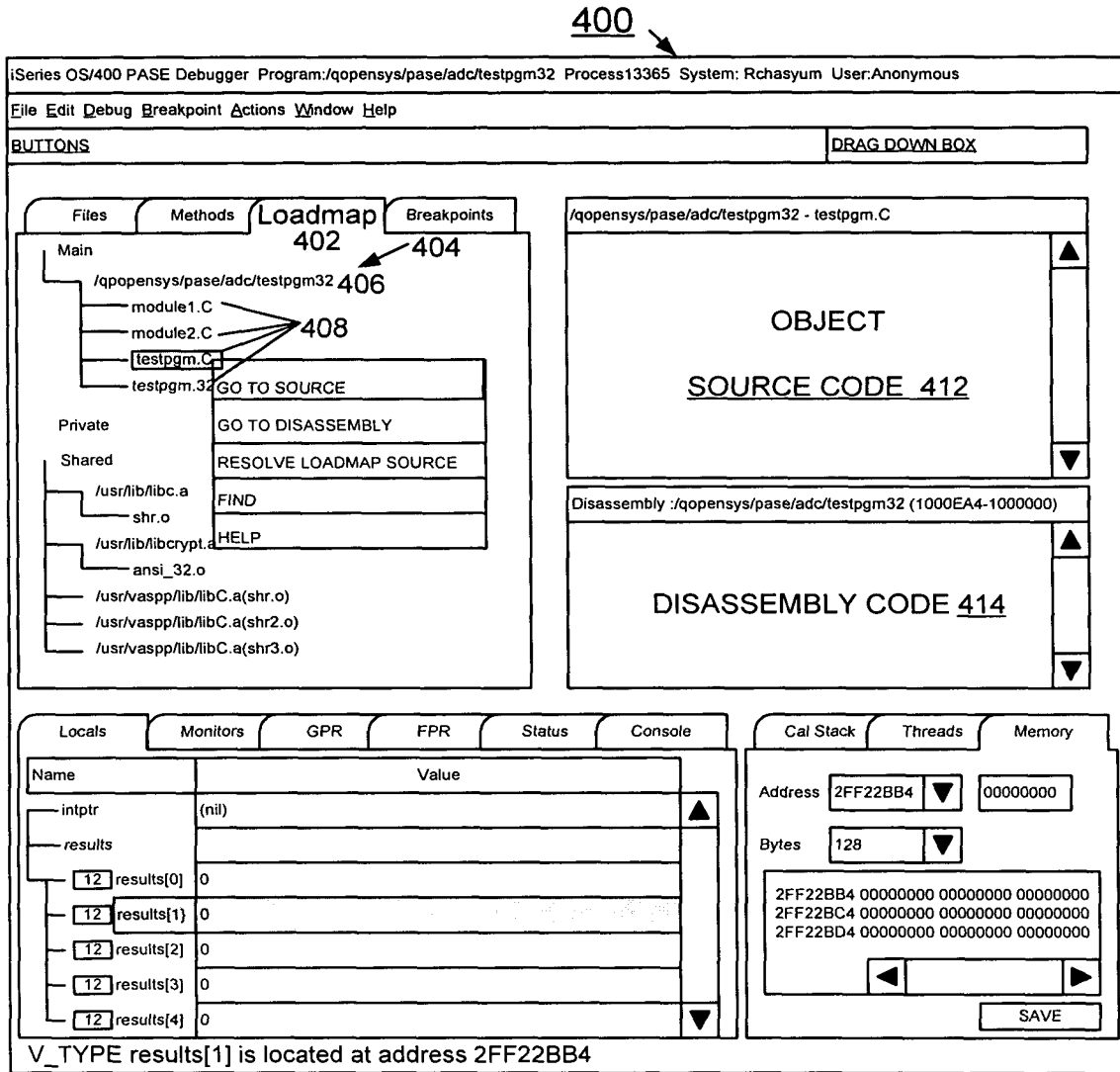
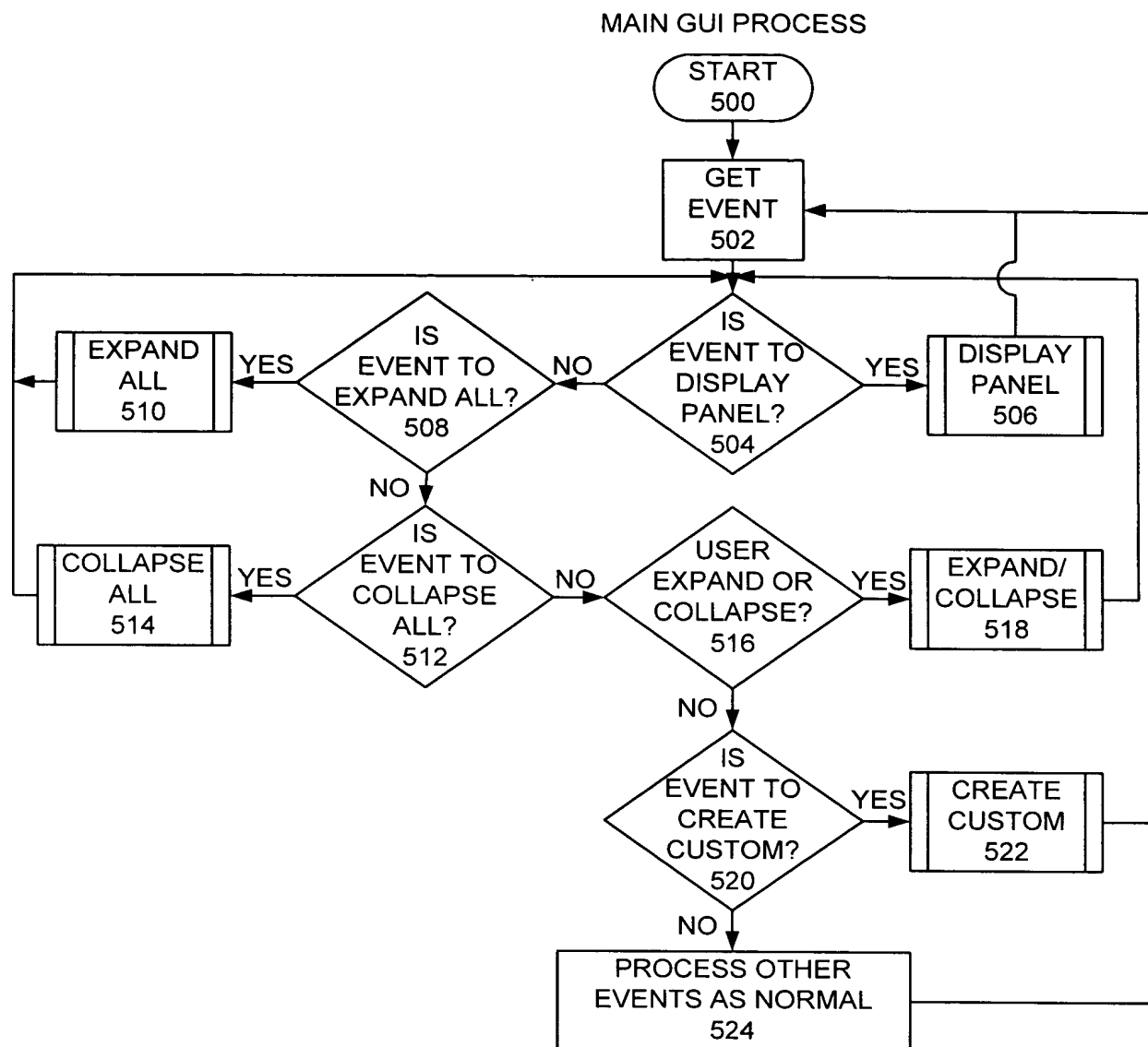


FIG. 4

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FIG. 5

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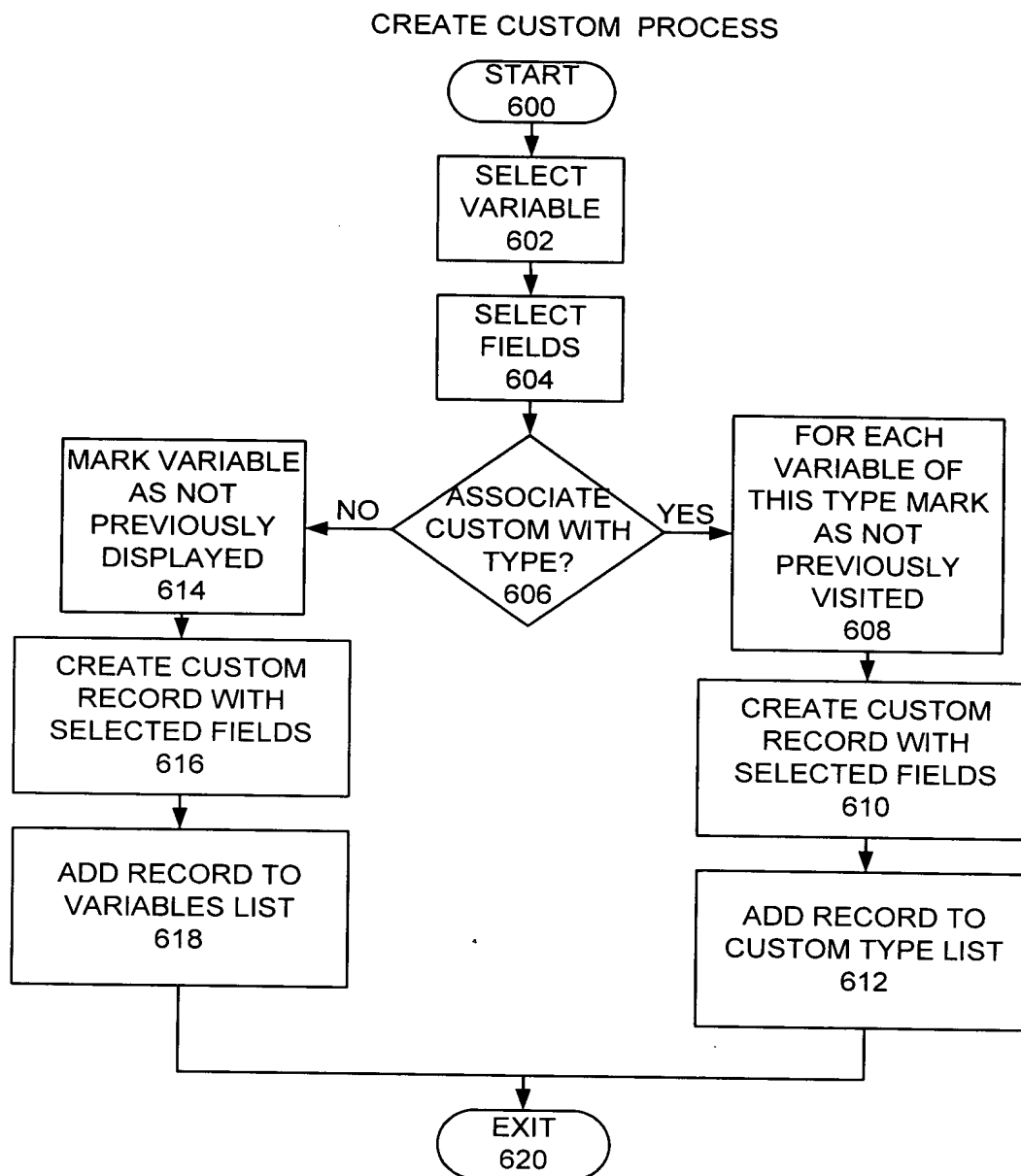


FIG. 6

DISPLAY PANEL PROCESS

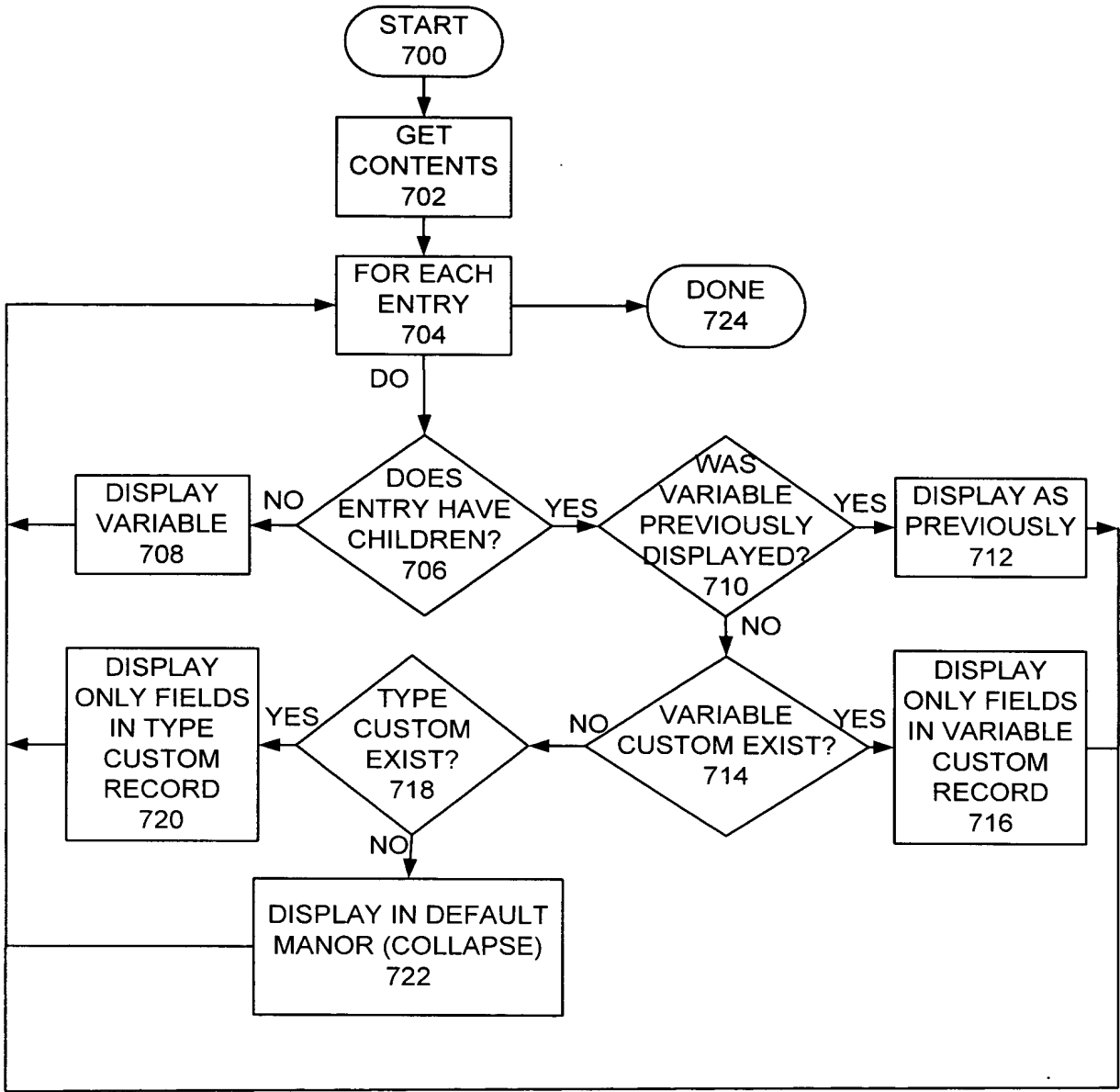


FIG. 7

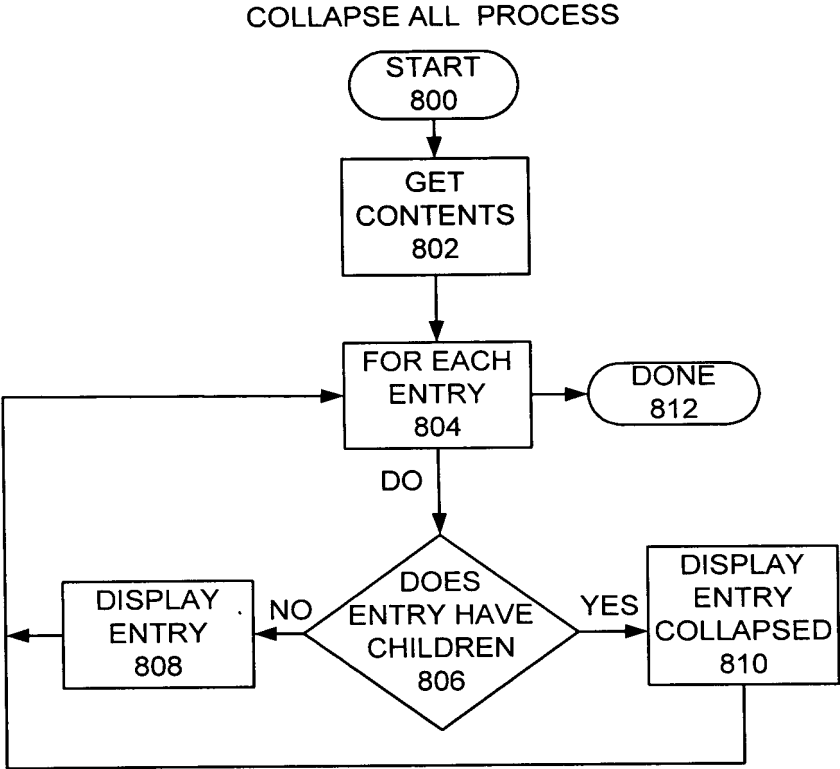
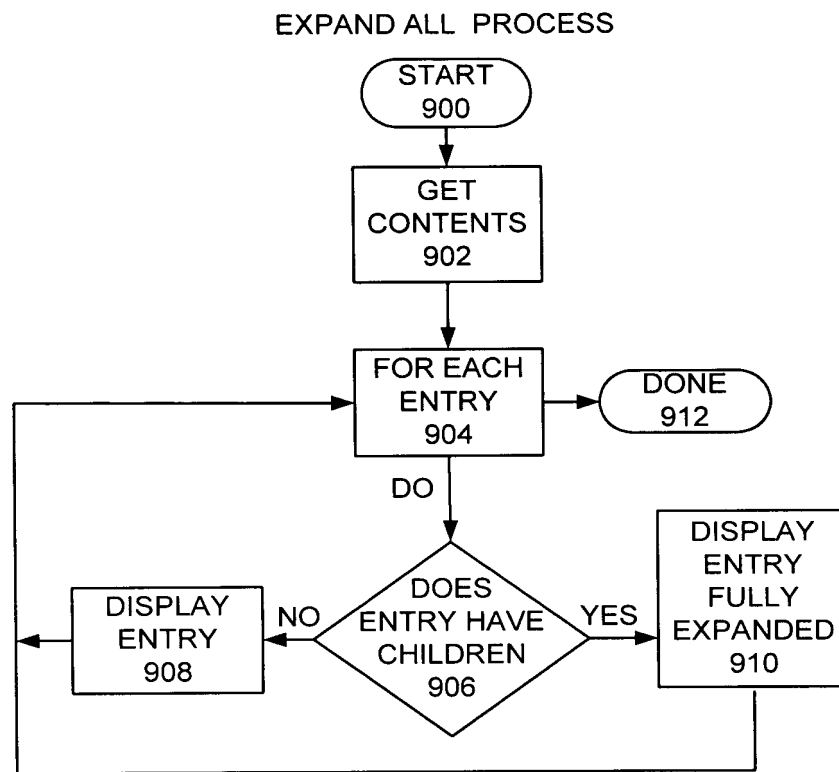
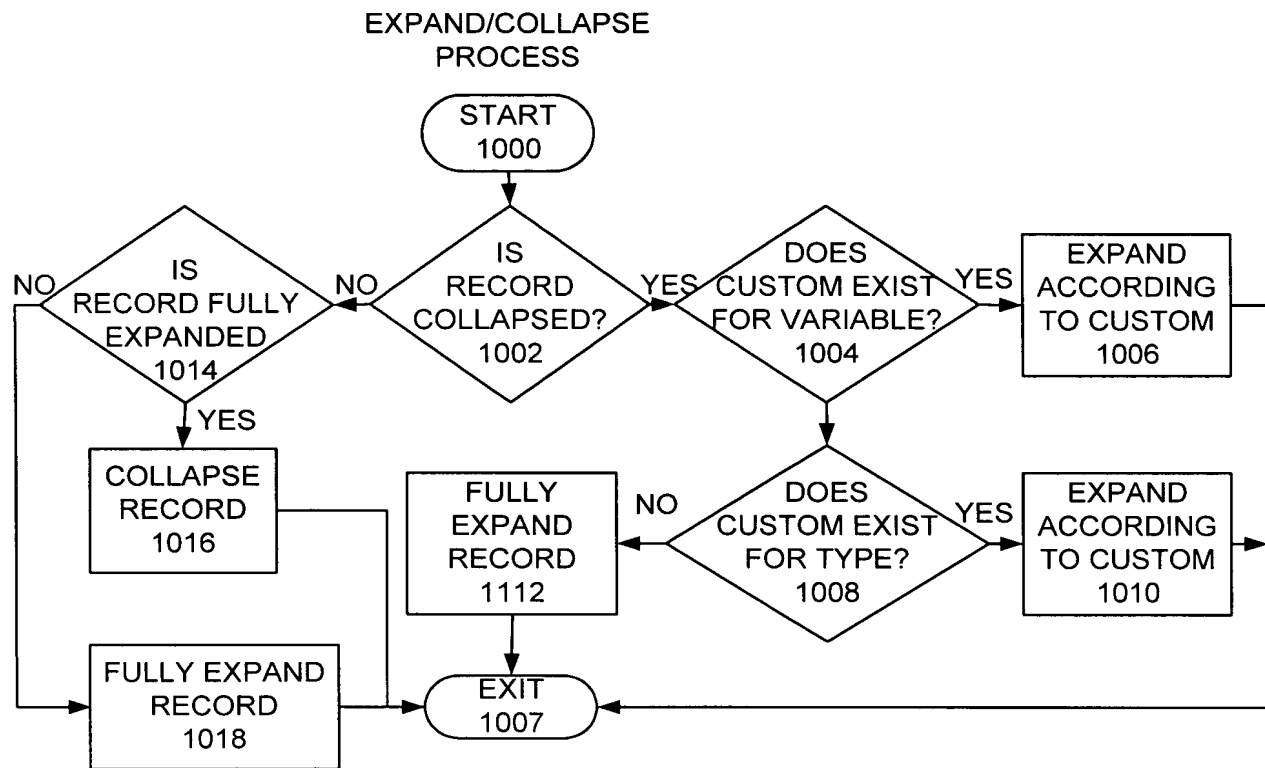


FIG. 8

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FIG. 9

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FIG. 10

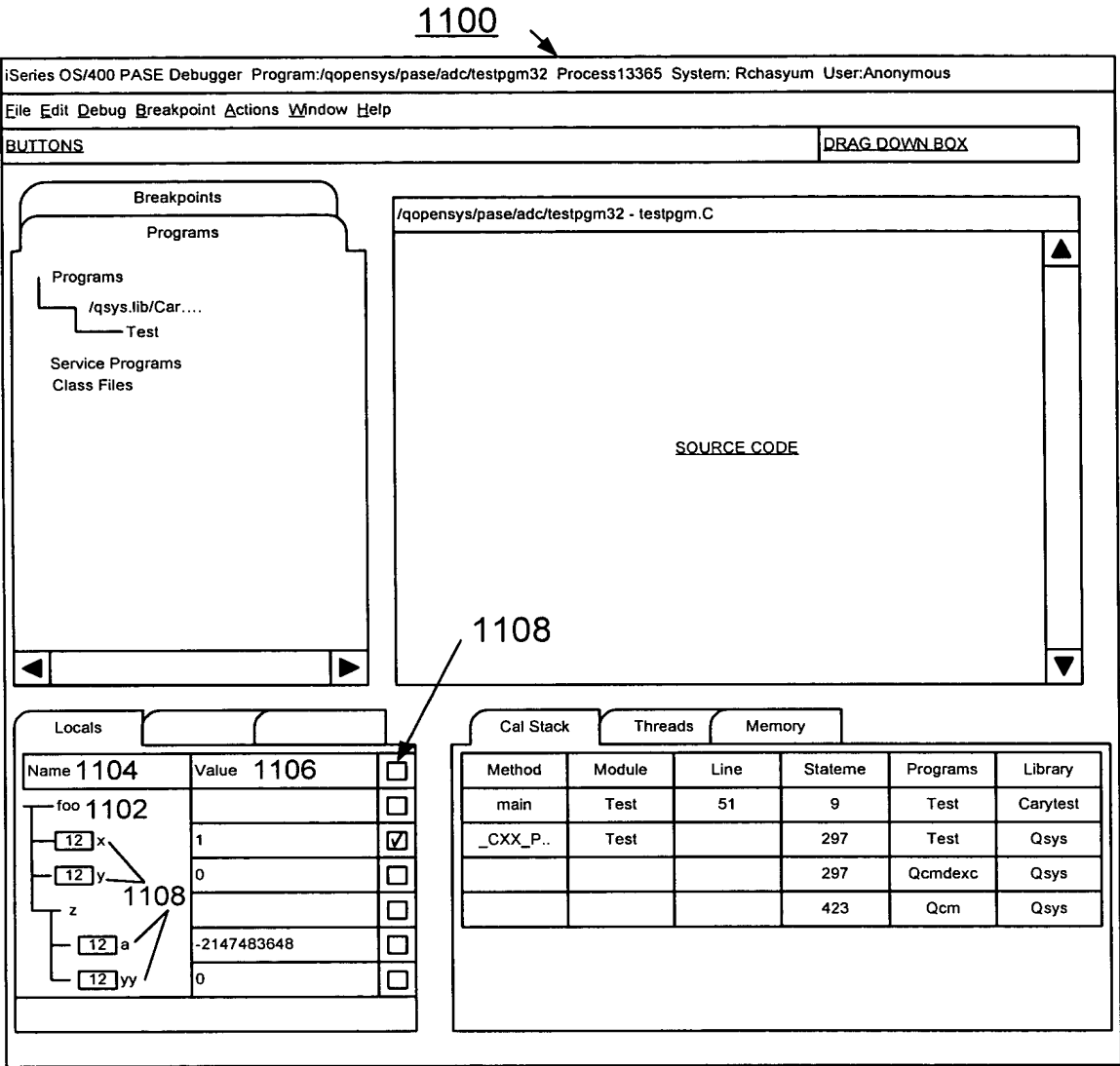


FIG. 11

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1200

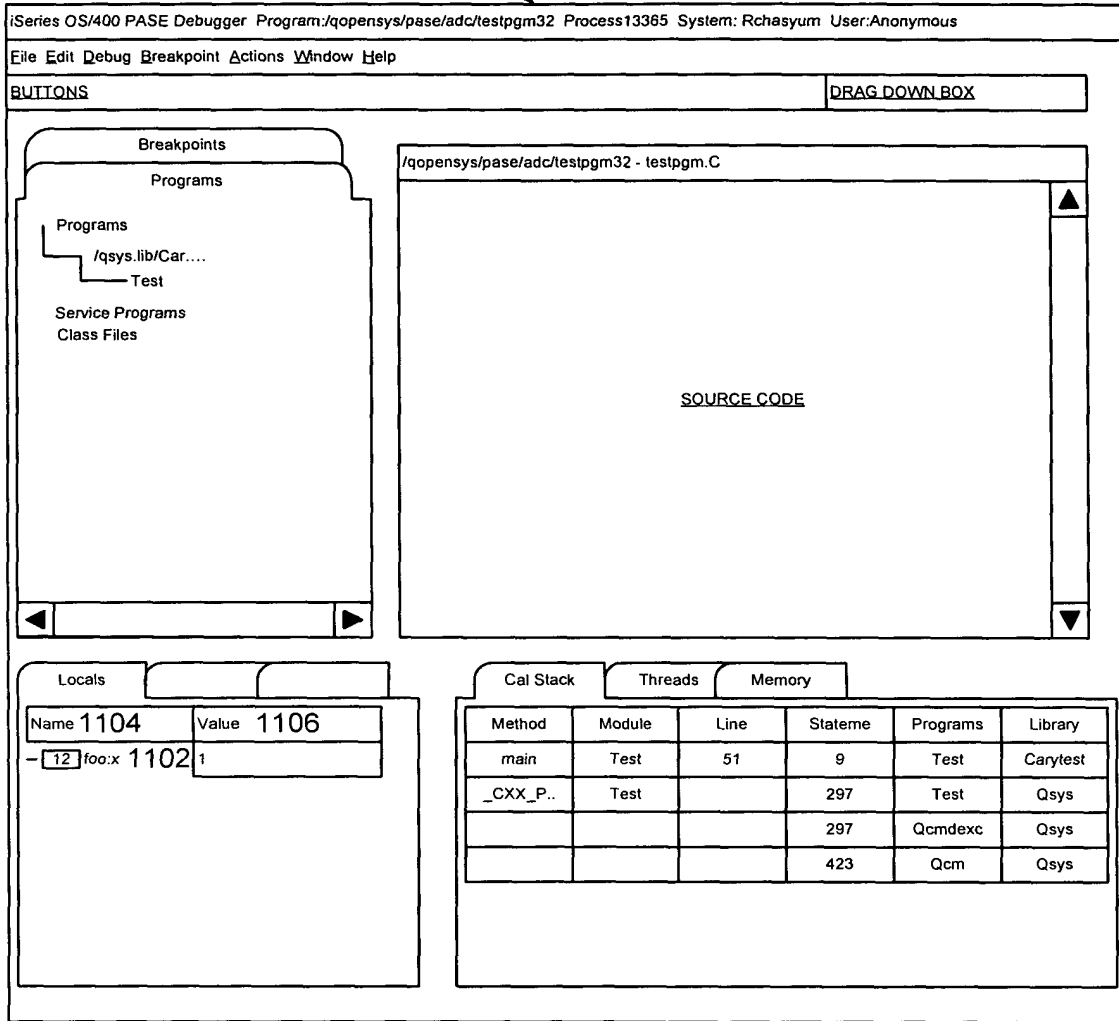


FIG. 12

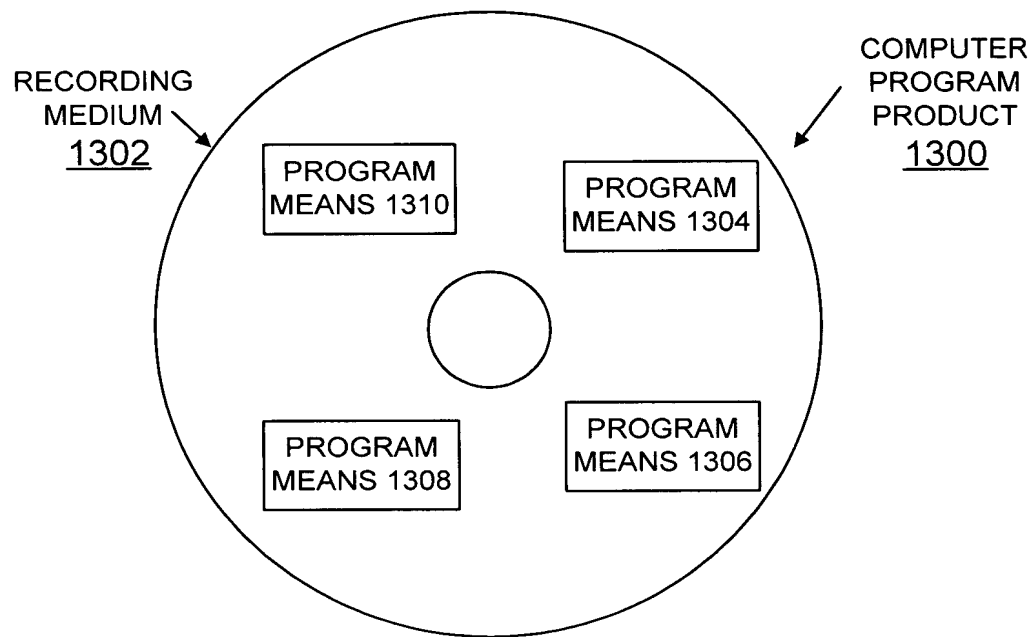


FIG. 13